



#20

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
**BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES**

In re Applicants:  
RABAH S HAMDI  
EDWARD E. OLKKOLA

Filed: March 31, 1998

Serial No.: 09/052,744

For: WIRELESS UNIVERSAL  
SERIAL BUS LINK FOR A  
COMPUTER SYSTEM

§ Art Unit: 2782  
§  
§  
§  
§ Examiner: ELAMIN, A.  
§  
§ Docket No.: P-1589

## CERTIFICATE OF MAILING

I hereby certify that this paper correspondence is being deposited with the United States postal service as Express Mail to Addressee Service under 37 CFR 1.10 mailing Label Number EL70425962US addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on March 26, 2001 by: *Lisa M. Wynn*

**APPEAL BRIEF**

**Russell C. Scott**  
**AKIN, GUMP, STRAUSS, HAUER**  
**& FELD, L.L.P.**  
**1900 Frost Bank Building**  
**816 Congress Avenue**  
**Austin, Texas 78701**  
**(512) 499-6200**  
**(512) 499-6290 Fax**

## TABLE OF CONTENTS

	Page
I. REAL PARTY IN INTEREST .....	1
II. RELATED APPEALS AND INTERFERENCES .....	2
III. STATUS OF THE CLAIMS .....	2
IV. STATUS OF AMENDMENTS .....	2
V. SUMMARY OF THE INVENTION .....	2
VI. ISSUES ON APPEAL .....	3
VII. GROUPING OF THE CLAIMS .....	3
VIII. ARGUMENT .....	3
A. Summary of Argument .....	3
B. The Cited Art Fails To Teach, Disclose, Or Otherwise Suggest A Peripheral Hub Device Including At Least A USB Host Port .....	4
1. Burnett Fails To Teach, Disclose, Or Otherwise Suggest A Peripheral Hub Device Including At Least A USB Host Port Having A Transceiver For Forming A Wireless USB Bus Link Between The Peripheral Hub Device And Another Transceiver .....	4
C. Burnett Fails To Teach, Disclose, Or Otherwise Suggest A Radio Frequency Transceiver Apparatus For Providing A Wireless USB Bus .....	5
IX. CONCLUSION .....	6

## APPENDIX

D. CLAIMS ON APPEAL .....	7
E. Burnett, et al. (U.S. Patent No. 5,870,080) .....	14



~~~~~

**Docket No.: P-1589**

1

## II.

### **RELATED APPEALS AND INTERFERENCES**

There are no other appeals or interferences which will directly affect or be directly affected by this Board's decision in the present appeal.

## III.

### **STATUS OF THE CLAIMS**

Claims 1-4, 6-8, and 10-24, are pending in the present application. Claims 1-4, 6-8, 10, 11, 14, 17, 18, 23, and 24 stand rejected and are the appealed claims. Claims 12, 13, 15, 16, and 19-22 were objected to. All claims are set forth in their present form in Appendix A to this Brief.

## IV.

### **STATUS OF AMENDMENTS**

Appellant last amended the claims in an Amendment filed November 17, 2000. The Advisory Action mailed December 22, 2000 specifically states that the Amendment will be entered upon filing of a Notice of Appeal and an Appeal Brief, thus, no amendments remain outstanding.

## V.

### **SUMMARY OF THE INVENTION**

A computer system including a computer that has at least an upstream USB port, and a peripheral hub device that has at least a USB host port. The upstream USB port of the computer has a first transceiver and the USB host port of the peripheral hub device has a second transceiver. The first and second transceivers cooperate to form a wireless USB bus link between the computer and the peripheral hub device.

Other aspects of the invention may be realized in a radio frequency transceiver apparatus that provides a wireless USB bus. The radio frequency transceiver includes a USB connector that connects to a hosting device, and a USB interface that is electrically connected to the USB connector. The radio frequency transceiver is electrically connected to the USB interface and the transceiver transmits and receives radio frequency signals. An antenna is operatively connected to the transceiver.

## VI.

### **ISSUES ON APPEAL**

Whether claims 1-4, 6-8, 10, 11, 14, 17, 18, 23, and 24 are unpatentable under 35 U.S.C. §103(a) over Burnett, et al. (U.S. Patent No. 5,870,080, hereinafter referred to as "Burnett").

## VII.

### **GROUPING OF THE CLAIMS**

The claims on appeal can be treated as a single group, however, as indicated in section VIII. ARGUMENT of this brief, the claims of the group do not stand or fall together.

## VIII.

### **ARGUMENT**

#### **A. Summary of Argument**

Appellant respectfully submits that in rejecting the claims under 35 U.S.C. §103(a), the Examiner has ignored Appellant's claimed elements which recite, among other things, a peripheral hub device that includes at least a USB host port that forms a wireless USB bus link with a computer. Further, the Examiner has failed to point to any suggestion within the prior art which associates a printer or similar device with a peripheral hub device.

First, the cited reference does not teach, disclose, or otherwise suggest a peripheral hub device including at least a USB host port. As will be shown, the peripheral hub device as recited in Appellant's independent claims 1 and 23 is completely different than the printer or similar device of the cited reference.

Second, the cited reference does not teach, disclose, or otherwise suggest a radio frequency transceiver apparatus for providing a wireless USB bus as recited in Appellant's independent claim 17. Appellant will show that the radio frequency apparatus operates transparently in the USB system and does not require host controller circuitry and does not operate as a USB device.

For at least the above and the following reasons, Appellant respectfully requests reversal of the final rejection of the appealed claims, the claims not standing or falling together and being separately patentable.

**B. The Cited Art Fails To Teach, Disclose, Or Otherwise Suggest A Peripheral Hub Device Including At Least A USB Host Port**

Appellant's claimed subject matter recites a peripheral hub device including at least a USB host port having a transceiver for forming a wireless USB bus link between the peripheral hub device and another transceiver coupled to a USB port of a computer (see, for example, claim 1). The cited reference fails to teach, disclose, or otherwise suggest such configuration.

**1. Burnett Fails To Teach, Disclose, Or Otherwise Suggest A Peripheral Hub Device Including At Least A USB Host Port Having A Transceiver For Forming A Wireless USB Bus Link Between The Peripheral Hub Device And Another Transceiver**

The Examiner asserts that Burnett, col. 7, lines 28-31, and 55-56, teaches a peripheral hub device and supports a 35 U.S.C. §103(a) rejection of claims 1 and 23. Appellant's illustrative claim 1 recites "a peripheral hub device including at least a USB host port" (Appellant's claim 1, line 3.).

The Examiner's argument in the Office action of September 26, 2000 correctly notes that Burnett's system, like many communication systems of the prior art, is capable of sending and receiving signals between transceivers (Paper No. 14, page 3, para. 8a); however, the argument goes astray when it argues that wireless communications between devices make the devices equivalent (see Paper No. 15, Interview Summary). Specifically, the peripheral hub device of Appellant's claim 1 includes, unlike a printer or similar device, a USB host port which, as understood by those of ordinary skill in the art, creates a hub for further communication with other devices such as a printer. For at least the above reason, it should be clear that a hub is completely different than a printer or similar device and that the Examiner's rejection should be reversed.

The cited art does not begin to teach or suggest Appellant's claim element of a peripheral hub device including at least a USB host port having a transceiver for forming a wireless USB bus link between the peripheral hub device and another transceiver coupled to a USB port of a computer. The Examiner has failed to recognize the significance of the language of peripheral hub device, which does not refer to printers or similar devices at all, but instead to networking devices such as switches and routers. Burnett expressly states that the invention pertains to communications between a transceiver in a pointing device and a printer or "similarly equipped peripheral device" (Burnett, col. 3, lines 60-63; col. 4, lines 63-64).

C. **Burnett Fails To Teach, Disclose, Or Otherwise Suggest A Radio Frequency Transceiver Apparatus For Providing A Wireless USB Bus**

In addition to the assertions refuted above that independent claims 1 and 23 were unpatentable under §103(a), the Examiner rejected independent claim 17 as being unpatentable under 35 U.S.C. §103(a) over Burnett.

For at least the reasons shown above, Burnett fails to appreciate the need for a wireless USB bus that operates transparently in the USB system and does not require host controller circuitry and does not operate as a USB device.

IX.

CONCLUSION

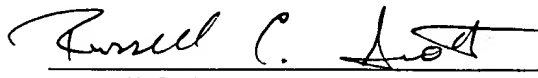
Among other reasons, for the reasons set forth in detail above, it is respectfully submitted that the rejection of claims 1-4, 6-8, 10, 11, 14, 17, 18, 23, and 24 under 35 U.S.C. 103(a) are clearly and manifestly erroneous.

As shown above, the cited reference fails to teach, disclose, or otherwise suggest a peripheral hub device including at least a USB host port having a transceiver for forming a wireless USB bus link between the peripheral hub device and another transceiver coupled to a USB port of a computer. Therefore Appellant's claimed subject matter cannot be anticipated or rendered obvious by the cited reference.

Reversal of the final rejection of claims 1-4, 6-8, 10, 11, 14, 17, 18, 23, and 24 is the appropriate action for this board. Such action is respectfully requested.

Respectfully submitted,

Date: 3/26/01

  
\_\_\_\_\_  
Russell C. Scott  
Registration No. 43,103

AKIN, GUMP, STRAUSS, HAUER & FELD, L.L.P.  
1900 Frost Bank Building  
816 Congress Avenue  
Austin, Texas 78701  
(512) 499-6200  
(512) 499-6290 Fax



## APPENDIX A

### CLAIMS ON APPEAL (AS AMENDED NOVEMBER 17, 2000)

1           1.     A computer system comprising:  
2           a computer including at least an upstream USB port;  
3           a peripheral hub device including at least a USB host port;  
4           a first transceiver coupled to the upstream USB port of said computer; and  
5           a second transceiver coupled to the USB host port of said peripheral hub device, said  
6 first and second transceivers cooperate to form a wireless USB bus link between said computer  
7 and said peripheral hub device.

1           2.     A computer system as recited in claim 1, wherein said computer includes a bus  
2 controller that controls the wireless USB bus link formed between said computer and said peripheral  
3 hub device.

1           3.     A computer system as recited in claim 1,  
2           wherein said computer has a housing, and  
3           wherein said first transceiver is provided internal to the housing of said computer.

1           4.     A computer system as recited in claim 1, wherein said computer has a housing, and  
2           wherein said first transceiver is provided external to the housing of said computer, and said first  
3 transceiver couples to the USB host port of said computer.

1           6.     A computer system as recited in claim 4,  
2                 wherein the peripheral hub device has a housing, and  
3                 wherein said second transceiver is provided internal to the housing of said peripheral  
4     hub device.

1           7.     A computer system as recited in claim 4,  
2                 wherein the peripheral hub device has a housing, and  
3                 wherein said second transceiver is provided external to the housing of said peripheral hub  
4     device, and said second transceiver couples to the upstream USB port of said peripheral hub  
5     device.

1           8.     A computer system as recited in claim 1, wherein said peripheral hub device has a  
2     plurality of downstream USB ports.

1           10.    A computer system as recited in claim 8, wherein said computer system further  
2     comprises  
3                 a printer, and wherein said printer is connected to one of the downstream USB ports of  
4     the peripheral hub device.

1           11.     A computer system as recited in claim 1, wherein said computer supplies a wired,  
2     internal USB bus to the USB host port of said computer, and wherein said first transceiver  
3     comprises:

4                     a first antenna;

5                     first transceiver circuitry for transmitting data at radio frequencies via said first  
6     antenna; and

7                     a first bus interface that interfaces said first transceiver circuitry to the internal  
8     USB bus.

1           12.     A computer system as recited in claim 11, wherein said first transceiver circuitry has  
2     a normal power mode and a low power mode, and the mode of said first transceiver circuitry is  
3     controlled by control signals supplied to said first transceiver via the internal USB bus.

1           13.     A computer system as recited in claim 11, wherein said first transceiver circuitry  
2     comprises:

3                     a first modulator that modulates and converts digital signals to be transmitted to produce  
4     analog signals of a first frequency;

5                     a first up-converter operatively connected between said first modulator and said first antenna,  
6     said first up-converter converts the analog signals of the first frequency to analog signals of a second  
7     frequency that is greater than the first frequency, and supplies the signals of the second frequency  
8     to said first antenna;

9                     a first down-converter that receives incoming analog signals of the second frequency from

10 said first antenna and converts the incoming analog signals to incoming analog signals of the first  
11 frequency that is less than the second frequency; and  
12 a first demodulator operatively connected to said first down-converter, said first demodulator  
13 demodulates the incoming analog signals to obtain incoming digital signals.

1 14. A computer system as recited in claim 11, wherein said second transceiver comprises:  
2 a second antenna;  
3 second transceiver circuitry for transmitting data at radio frequencies via said second  
4 antenna; and  
5 a second bus interface that interfaces said second transceiver circuitry to the wireless  
6 USB bus.

1 15. A computer system as recited in claim 14, wherein said second transceiver circuitry  
2 has a normal power mode and a low power mode, and the mode of said second transceiver circuitry  
3 is controlled by control signals supplied to said second transceiver via the wireless USB bus.

1 16. A computer system as recited in claim 14, wherein said second transceiver circuitry  
2 comprises:  
3 a second modulator that modulates and converts digital signals to be transmitted to produce  
4 analog signals of a first frequency;  
5 a second up-converter operatively connected between said second modulator and said second

6 antenna, said second up-converter converts the analog signals of the first frequency to analog signals  
7 of a second frequency that is greater than the first frequency, and supplies the signals of the second  
8 frequency to said second antenna;

9 a second down-converter that receives incoming analog signals of the second frequency from  
10 said second antenna and converts the incoming analog signals to incoming analog signals of the first  
11 frequency that is less than the second frequency; and

12 a second demodulator operatively connected to said second down-converter, said second  
13 demodulator demodulates the incoming analog signals to obtain incoming digital signals.

1 17. A radio frequency transceiver apparatus for providing a wireless USB bus, said  
2 transceiver apparatus comprising:

3 a USB connector for connecting to a hosting device;

4 a USB interface electrically connected to said USB connector;

5 a radio frequency transceiver electrically connected to said USB interface, said transceiver  
6 transmits and receives radio frequency signals ; and

7 an antenna operatively connected to said transceiver.

1 18. A radio frequency transceiver apparatus as recited in claim 17, wherein said USB  
2 connector is used to couple to a USB port of a computer system having a USB host controller.

1           19.    A radio frequency transceiver apparatus as recited in claim 17, wherein said  
2 transceiver comprises:

3           a modulator that modulates digital signals to be transmitted to produce analog signals of a  
4 first frequency;

5           an up-converter operatively connected between said modulator and said antenna, said up-  
6 converter converts the analog signals of the first frequency to analog signals of a second frequency  
7 that is greater than the first frequency, and supplies the signals of the second frequency to said  
8 antenna;

9           a down-converter that receives incoming analog signals of the second frequency from said  
10 antenna and converts the incoming analog signals to incoming analog signals of the first frequency  
11 that is less than the second frequency; and

12          a demodulator operatively connected to said down-converter, said demodulator demodulates  
13 the incoming analog signals to obtain incoming digital signals.

1           20.    A radio frequency transceiver apparatus as recited in claim 19, wherein said  
2 modulator modulates the digital signals using QPSK modulation.

1           21.    A radio frequency transceiver apparatus as recited in claim 19, wherein said  
2 transceiver further comprises:

3           a phase-lock-loop that synchronizes with the incoming analog signals to synchronize  
4 with the first frequency.

1        22.    A radio frequency transceiver apparatus as recited in claim 21,  
2        wherein said transceiver apparatus has an active state and a low power state, and  
3        wherein said transceiver apparatus further comprises:  
4                a power management unit that places said transceiver apparatus in one of the low  
5        power state and the active state.

1        23.    A method for transmitting data over a USB bus from a computer to a peripheral  
2        hub device, comprising:  
3                providing a first transceiver at the computer, the first transceiver being coupled to a USB  
4        host controller that controls a USB bus for the computer;  
5                providing a second transceiver at an upstream port of the peripheral hub device; and  
6                establishing a wireless USB bus link between the first and second transceivers, the wireless  
7        USB bus link being part of the USB bus.

1        24.    A method as recited in claim 23, wherein said method further comprises:  
2        managing power utilization of the first and second transceivers.



US005870080A

**United States Patent** [19]

Burnett et al.

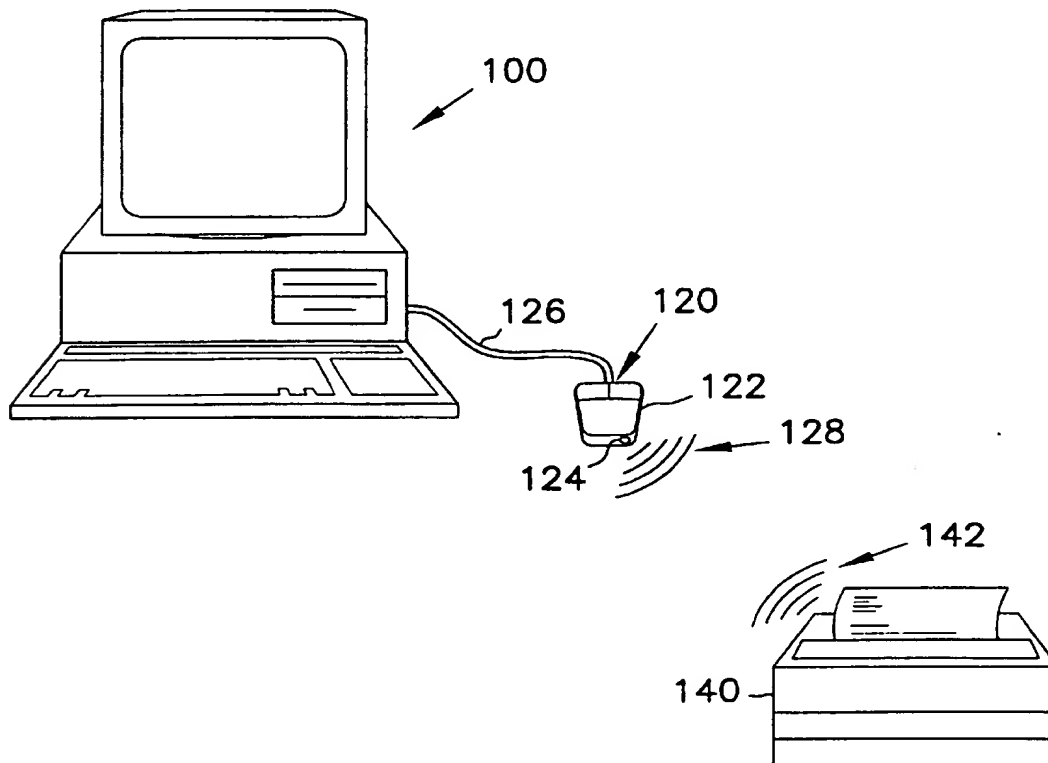
[11] **Patent Number:** 5,870,080[45] **Date of Patent:** Feb. 9, 1999[54] **ELECTRO-MAGNETIC TRANSCEIVER  
COMBINED WITH A POINTING DEVICE**[75] **Inventors:** Robert J. Burnett, Dakota Dunes, S.  
Dak.; Ajay Gupta, Sioux City, Iowa[73] **Assignee:** Gateway 2000, Inc., North Sioux City,  
S. Dak.[21] **Appl. No.:** 615,421[22] **Filed:** Mar. 14, 1996[51] **Int. Cl.<sup>6</sup>** ..... G09G 5/08[52] **U.S. Cl.** ..... 345/163; 345/158; 395/893[58] **Field of Search** ..... 345/156, 157,  
345/158, 161, 163-167, 168, 179; 341/20;  
359/142, 143, 145, 148, 154, 172; 375/222;  
395/882, 884, 885, 892, 893, 309[56] **References Cited****U.S. PATENT DOCUMENTS**

|           |         |              |         |
|-----------|---------|--------------|---------|
| 4,750,113 | 6/1988  | Buggert      | 395/884 |
| 4,754,268 | 6/1988  | Mori         | 345/163 |
| 4,924,216 | 5/1990  | Leung        | 345/158 |
| 5,075,792 | 12/1991 | Brown et al. | 359/152 |
| 5,081,711 | 1/1992  | Rickman, Jr. | 359/146 |
| 5,262,777 | 11/1993 | Low et al.   | 341/20  |

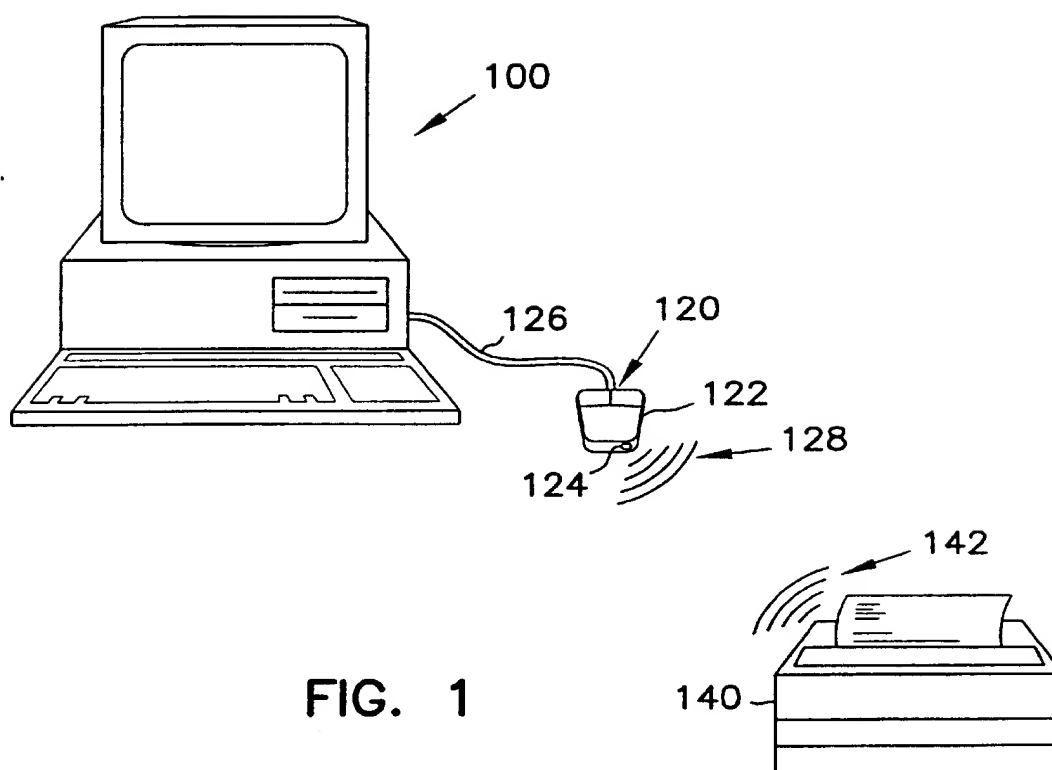
|           |         |                        |         |
|-----------|---------|------------------------|---------|
| 5,339,095 | 8/1994  | Redford                | 345/158 |
| 5,394,168 | 2/1995  | Smith, III et al.      | 345/158 |
| 5,400,246 | 3/1995  | Wilson et al.          | 345/168 |
| 5,557,634 | 9/1996  | Balasubramanian et al. | 375/222 |
| 5,564,020 | 10/1996 | Rossi                  | 395/885 |
| 5,615,083 | 3/1997  | Burnett                | 345/161 |
| 5,617,236 | 4/1997  | Wang et al.            | 359/172 |
| 5,706,031 | 1/1998  | Brendzel et al.        | 345/172 |

*Primary Examiner*—Steven J. Saras*Assistant Examiner*—David Lewis*Attorney, Agent, or Firm*—Schwegman, Lundberg,  
Woessner & Kluth, P.A.; Anthony Claiborne[57] **ABSTRACT**

A computer peripheral combines an electro-magnetic transceiver and a computer pointing device in the same housing. The pointing device cable provides the electrical connection between the electro-magnetic transceiver and the computer's data bus. The transceiver receives information in the form of electro-magnetic pulses from an appropriately-equipped remote device, converts the pulses to electrical signals, and relays the electrical signals to the computer. The transceiver receives electrical signals addressed to the device from the computer, converts the electrical signals to electro-magnetic pulses, and relays the pulses to the remote device.

**8 Claims, 2 Drawing Sheets****APPENDIX B**





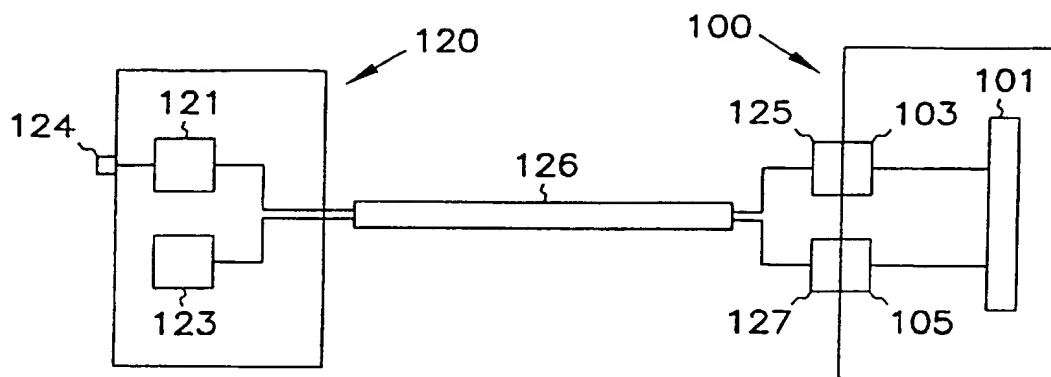


FIG. 2

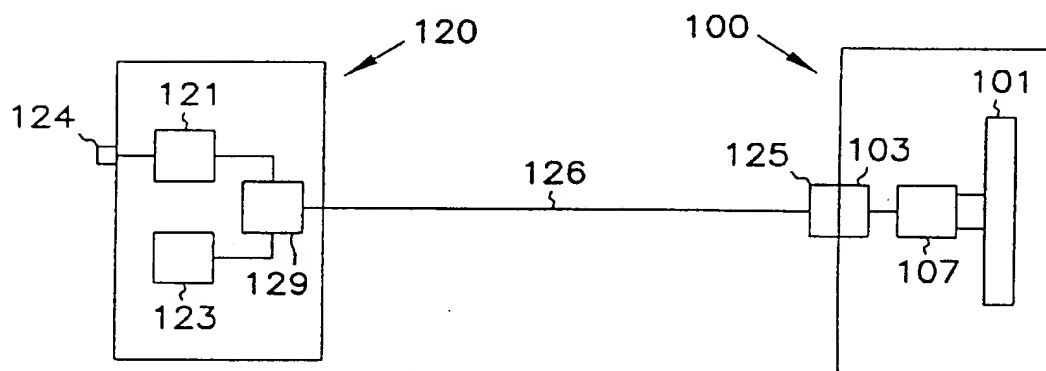


FIG. 3

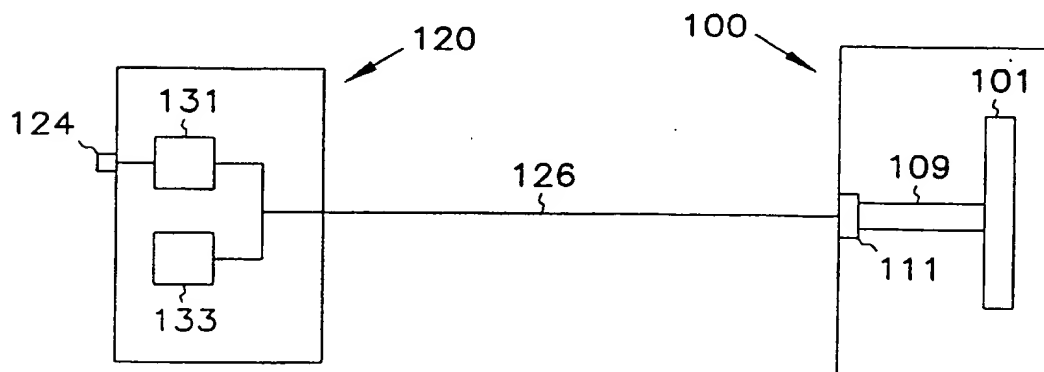


FIG. 4

## ELECTRO-MAGNETIC TRANSCEIVER COMBINED WITH A POINTING DEVICE

### FIELD OF THE INVENTION

The present invention is related to computer systems and in particular to a computer peripheral combining an electro-magnetic transceiver and a computer pointing device in the same housing for attachment to a computer through an I/O channel.

### BACKGROUND OF THE INVENTION

As the number of peripherals users attach to their computer systems proliferate, the more dense the tangle of cables around the computer becomes.

Besides being bulky, cables also limit the maximum distance the peripherals may be located from the computer. Peripheral manufacturers have addressed these problems by communicating commands and data through electro-magnetic (EM) pulses which do not require any type of physical connection between the peripheral and the computer. Electro-magnetic communication requires two electronic circuits. The first, called an EM transmitter, converts electrical signals into EM pulses and broadcasts the EM pulses into the atmosphere. A corresponding electronic circuit, called an EM receiver, collects the pulses and converts them back into electrical signals. In theory, EM communication could use infrared (IR) light, radio frequency (RF) waves, or any other portion of the electro-magnetic spectrum. As a practical matter, however, only IR and RF are suitable for computer communication without causing or being impeded by interference from other radiation sources. Most manufacturers have chosen to implement EM technology using IR but the following discussion applies to RF as well.

The most basic IR connection is used in one-way communication: the computer sends commands and data to the peripheral but the peripheral does not send information back to the computer. A one-way IR communication link has an IR transmitter coupled to the data bus of the computer and an IR receiver coupled to the input line of the peripheral device. The IR transmitter converts the standard electrical signals representing commands and data for the device into pulses of infrared light and transmits those pulses across some distance to the IR receiver. The IR receiver then converts the pulses back into standard electrical signals and passes them onto the device where they are acted upon as either commands or data.

A more complicated IR connection provides for bi-directional communication between the computer and the peripheral, so the computer and the device must each have both an IR transmitter and an IR receiver. In such cases, the IR transmitter and IR receiver circuitry is usually combined into a single unit called an IR transceiver.

Until recently, all IR-equipped peripherals used proprietary technology so each device required its own unique IR transceiver connected to the computer. In 1994, however, an industry organization called the Infrared Data Association (IRDA) proposed an IR standard that promotes compatibility among IRDA compliant devices. Such compatibility allows a computer with a single IRDA transceiver to communicate with multiple IRDA devices such as printers, modems, fax machines, and even other IRDA-equipped computers. The IRDA standard also specifies error checking and correction protocols and enables a 9600 KB to 4 MB per second transfer rate.

If using proprietary IR circuitry, a peripheral manufacturer provides an external unit containing the corresponding

proprietary IR circuit for the computer. This external unit, often referred to as a "dongle", interfaces to the computer by means of a cable connected through a serial, or other suitable, port. Since the IRDA standard was promulgated, many vendors have announced they will build IRDA compliant computers but existing computer users want the advantages offered by IRDA. Therefore, a dongle incorporating the IRDA specification will be an important add-on device for many computers.

By using IRDA compliant peripherals and an IRDA dongle, users can reduce the number of peripheral cables they have connected to their computer. However, the IRDA dongle does not completely eliminate all peripheral cables since it itself requires a cable connected to a serial port. Also, most users have a pointing device connected by another cable to the computer, usually to a serial port, and a keyboard connected by yet another cable. The need to connect the IRDA dongle and the pointing device through separate serial ports impairs the user's ability to attach other serial devices as most computers are configured with only two serial ports. A further limitation of the IRDA dongle is that it competes with the pointing device for space in the user's often crowded work area. Thus, there is a need to reduce the space required for such dongles.

### SUMMARY OF THE INVENTION

The present invention captures the advantages of an electro-magnetic (EM) dongle, eliminates the dongle cable, and requires no additional space on the user's desk by combining an EM transceiver with control circuitry for a pointing device in a standard mouse, trackball, or similar housing. The standard pointing device cable provides the electrical connection between the transceiver and a computer's data bus.

In one embodiment of the pointing device, the housing for the pointing device contains an EM transceiver and the pointing device control circuit. The EM transceiver and the pointing device control circuit are separately coupled to the data bus of a standard desktop computer. Two different pairs of wires run through the pointing device cable and terminate in two separate serial I/O port plugs (such as those used for RS232 communication), one for the electrical signals from the transceiver and one for the signals from the pointing device. Each I/O port plug connects to a serial port on the computer. In an alternate embodiment, the pointing device wire pair terminates in a PS2 mouse port plug for a computer having a PS2 mouse port.

The transceiver has a sensor/emitter portion that captures EM pulses sent from a compatible or corresponding transceiver in a peripheral device which the transceiver converts into electrical signals. The transceiver then sends the electrical signals via the transceiver wire in the cable to the serial I/O port. The computer's data bus in turn retrieves the signals from the I/O port and relays them to the computer's main processor. When the computer's main processor has commands or data to send to the peripheral, the process is reversed. The transceiver receives the appropriate electrical signals through its wires in the cable, converts the signals to EM pulses, and relays the pulses to the device via the sensor/emitter.

The configuration of other embodiments depend on the type of EM pulses used. In one alternate embodiment, the EM pulses are infrared (IR) light and the sensor/emitter is positioned on the outside of the pointing device housing to collect and broadcast the IR pulses. A further embodiment using IR pulses comprises a transceiver compatible with the

3

Infrared Data Association standard to allow a single transceiver to communication with multiple IRDA compatible devices. In another alternate embodiment, the EM pulses are RF waves and the sensor/emitter is a small antenna positioned on the outside of the housing. In a further alternate embodiment using RF pulses, the antenna is placed inside the housing. And in a still further embodiment using RF pulses, the antenna is incorporated into the cable by having the cable itself serve as the antenna or through an antenna lead woven into the cable sheathing.

In another group of embodiments, the connection between the EM transceiver and the computer differs. In one alternate embodiment, the pointing device is configured to connect to a computer equipped with an intelligent serial I/O bus designed to the Universal Serial Bus (USB) specifications. The pointing device and the transceiver are USB compatible circuits having the ability to identify themselves to the USB compatible bus and are coupled to it through a USB port (called a "hub"). The USB compatible bus translates the device identification into a code the computer's processor recognizes as a device address. Because of the intelligence embedded in each device and in the USB compatible bus, a single I/O channel from the pointing device to the USB hub services both the pointing device and transceiver circuits. Furthermore, using USB compatible devices allows the invention to take advantage of the 4 MB transfer rates provided under the USB specification thus speeding the data flow to and from the computer.

In still another embodiment, the electrical signals from the pointing device and the transceiver are fed into a multiplexer/demultiplexer circuit (MUX/DMUX) located in the pointing device. The multiplexer portion of the MUX/DMUX interleaves the two signals together into a composite electrical signal and sends the composite signal via a single pair of wires to a serial I/O port on the computer. A corresponding MUX/DMUX is coupled between the serial I/O port and the data bus in the computer. The demultiplexer portion of the MUX/DMUX in the computer unpacks the composite electrical signal into the original signal sent by the transceiver and the original signal sent by the pointing device, and routes the two signals to the data bus. The process is reversed to send commands or data from the computer to the peripheral by using the multiplexer portion of the MUX/DMUX in the computer and the demultiplexer in the MUX/DMUX in the pointing device.

As has been discussed above, three drawbacks of a standard EM dongle is that it requires a cable connection to the computer, competes with the user's pointing device for space on the desk, and takes up a serial port. The present invention addresses those drawbacks by combining the EM circuitry for the dongle in the same housing as the pointing device thus returning the lost space to the user and also eliminating one of the tangle of cables around the computer. In addition, the mouse and the EM dongle can share a single serial port to provide the user with flexibility in attaching additional serial devices to the computer.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a perspective view of the present invention illustrating electro-magnetic communication between an electro-magnetic (EM) transceiver in a pointing device and an EM-enabled printer.

FIG. 2 is a block diagram of the electrical connections in an embodiment of the present invention that uses separate I/O channels for sending electrical signals from the EM transceiver and the pointing device to the computer.

4

FIG. 3 is a block diagram of the electrical connections in an embodiment of the present invention that multiplexes the EM transceiver electrical signals and the pointing device electrical signals into a single I/O channel.

FIG. 4 is a block diagram of the electrical connections in an embodiment of the present invention coupled to a computer having an intelligent serial I/O bus compatible with Universal Serial Bus specifications.

#### DESCRIPTION OF THE EMBODIMENTS

In the following detailed description of the embodiments, reference is made to the accompanying drawings which form a part hereof, and in which is shown by way of illustration specific embodiments in which the invention may be practiced. These embodiments are described in sufficient detail to enable those skilled in the art to practice the invention, and it is to be understood that other embodiments may be utilized and that structural, logical and electrical changes may be made without departing from the spirit and scope of the present inventions. The following detailed description is, therefore, not to be taken in a limiting sense, and the scope of the present inventions is defined only by the appended claims.

The same components may appear in multiple Figures with the same number.

FIG. 1 shows a perspective view of a desktop computer 100 with a mouse 120 and a printer 140. Computer 100 also includes a keyboard, a monitor, memory and a processor. All these components are common in the industry and so will not be described in detail. The mouse 120 has a housing 122 and a cable 126 connecting it to the computer 100. A standard electro-magnetic (EM) transceiver circuit is located inside the mouse 120 and has a sensor/emitter portion 124 external to the housing. The computer 100 sends electrical signals representing commands and data for the printer via the cable 126 to the transceiver. The transceiver converts the electrical signals into EM pulses 128 and broadcasts the pulses 128 to the printer 140 by means of the sensor/emitter 124. The printer 140 is equipped with a corresponding EM transceiver with a sensor/emitter which converts the EM pulses 128 into printer command and data signals. Transceiver circuits using sensors and emitters, such as model numbers BPV22BF (sensor) and TS1P5201 (emitter) from Telefunken of Germany, are standard off-the-shelf components in the industry and are not discussed further.

When the printer 140 has information, such as "load paper", to send to the computer 100, the EM transceiver in the printer 140 converts the information into EM pulses 142 and broadcasts them. The EM transceiver in the mouse collects the EM pulses 142, converts them into electrical signals and sends them via the cable 126 to the computer 100.

As will be readily apparent to those skilled in the art, the electro-magnetic (EM) communication link between the computer and peripheral can be implemented in many different ways. A bi-directional link is shown in FIG. 1 but a uni-directional link, where only the computer sends commands and data to the peripheral, is a further embodiment of the present invention. A mouse is shown in FIG. 1 as the pointing device but track balls, digitizers, joysticks, light pens, or other similar devices are alternate embodiments. While FIG. 1 illustrates the invention using a printer, the use of other similarly equipped peripheral devices will be apparent to those skilled in the art. Electro-magnetic communication can use infrared (IR) light, radio frequency (RF) waves, or any other portion of the electro-magnetic spec-

trum. One embodiment of the present invention uses IR; another embodiment uses RF. Yet another embodiment uses a transceiver manufactured to the IR standard proposed by the Infrared Data Association ("IRDA") allowing more than one IRDA compatible peripheral to be controlled by the same transceiver in the pointing device.

FIGS. 2 through 4 are block diagrams of the electrical connections for different embodiments of the invention. In FIGS. 2 and 3, an industry standard EM transceiver 121 is located in the mouse 120 along with industry standard mouse control circuitry 123. Mouse control circuitry 123 is the standard circuitry configured within an off-the-shelf mouse, such as the Microsoft mouse part number 58269, which determines the position and command state of the mouse. The EM sensor/emitter portion 124 of the transceiver 121 is positioned on the outside of the mouse 120. In FIG. 4, an EM transceiver 131 and mouse control circuitry 133 compatible with Universal Serial Bus protocols (as described below) replace EM transceiver 121 and mouse control circuitry 123, respectively. It will be apparent to a person skilled in the art that alternate embodiments of FIGS. 2 through 4 are possible by changing the type of EM pulse.

In the embodiment shown in FIG. 2, two pairs of wires, one pair coupled to the transceiver 121 and one pair to the mouse circuit 123, are bound into a single cable 126 which branches into a Y junction at the opposite end. The wire pair from the transceiver 121 is terminated by an I/O port plug 125 on one branch of the Y and the wire pair from the mouse circuit 123 is terminated by an I/O port plug 127 on the other branch. I/O port plugs 125 and 127 connect respectively to I/O ports 103 and 105 in the computer 100. I/O ports 103 and 105 are two of a plurality of I/O ports coupled to the computer's data bus 101. In one embodiment, plug 125 is a serial port plug and port 103 is a serial port, while plug 127 is a PS2 mouse port plug and port 105 is a PS2 mouse port. In another embodiment, plugs 125 and 127 are both serial port plugs and port 103 and 105 are both serial ports.

An alternate embodiment in which the electrical signals from the transceiver 121 and from the mouse circuitry 123 are fed into a serial multiplexer/demultiplexer circuit 129 (MUX/DMUX) is shown in FIG. 3. The multiplexer portion of MUX/DMUX 129 interleaves the two electrical signals together into a composite electrical signal and sends the composite electrical signal via a single pair of wires in the mouse cable 126 to serial plug 125 connected to serial port 103 in the computer 100. A corresponding MUX/DMUX 107, coupled between the serial port 103 and the data bus 101 of the computer 100, unpacks the composite electrical signal into the original electrical signal sent by the transceiver 121 and the original electrical signal sent by the mouse circuitry 123 and feeds the unpacked signals to the data bus 101. The process is reversed to send commands or data from the computer 100 to the printer 140 by using the multiplexer portion of MUX/DMUX 107 and the demultiplexer in the MUX/DMUX 129. The signals may be multiplexed using frequency division multiplexing, time division multiplexing, statistical multiplexing, or a combination of methods incorporating industry standard multiplexing protocols supported by commonly available multiplexer/demultiplexer circuits. Other methods of multiplexing the signals will be apparent to those skilled in the art upon reading the above description.

FIG. 4 shows a computer 100 equipped with an intelligent serial I/O bus 109 compatible with the Universal Serial Bus (USB) specification currently being reviewed for implementation by several industry vendors. The USB specification is an adaption of existing two-line serial connections and

defines a universal connector and a single protocol for all USB compatible devices. The pointing device 133 and the transceiver 131 circuits are USB compatible and capable of identifying themselves to the USB compatible bus. Because of this device identification protocol, the signals from both the pointing device 123 and the transceiver 121 can be sent via a single pair of wires in a USB compatible cable 126 to USB port 111. The device identification logic embedded in the USB compatible bus 109, which is coupled to the USB port 111, determines which device sent the signal and translates the device identification into a device address recognizable to the computer's processor. USB compatible device Manufacturers such as Microsoft and Logitech have announced plans to make devices compatible with the USB specification.

It is to be understood that the above description is intended to be illustrative, and not restrictive. Many other embodiments will be apparent to those of skill in the art upon reviewing the above description. The scope of the invention should, therefore, be determined with reference to the appended claims, along with the full scope of equivalents to which such claims are entitled.

What is claimed is:

1. A peripheral device for a computer comprising:

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, wherein the pointing device circuitry is electrically coupled to the computer for transmitting the position and command state of the pointing device to a processor in the computer and further coupled to a first I/O port plug by means of a first plurality of wires, and the first I/O plug is coupled to a first I/O port on the computer; and

an electro-magnetic transceiver positioned within the housing for converting electro-magnetic pulses to and from electrical signals representative of commands and data, the transceiver being electrically coupled to the processor for relaying such electrical signals between the transceiver and the processor and further coupled to a second I/O port plug by means of a second plurality of wires, and the second I/O port plug is coupled to a second I/O port on the computer, the transceiver comprising at least one sensor/emitter for sending and receiving electro-magnetic pulses intended for further peripheral devices,

wherein the first and second plurality of wires are bound together into a cable having the first and second I/O port plugs separated out of the cable at one end, the electro-magnetic pulses comprise radio frequency waves, and the sensor/emitter comprises an antenna lead incorporated into the cable.

2. A peripheral device for a computer comprising:

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, wherein the pointing device circuitry is electrically coupled to the computer for transmitting the position and command state of the pointing device to a processor in the computer; and

an electro-magnetic transceiver positioned within the housing for converting electro-magnetic pulses to and from electrical signals representative of commands and data, the transceiver being electrically coupled to the processor for relaying such electrical signals between the transceiver and the processor, the transceiver comprising at least one sensor/emitter for sending and receiving electro-magnetic pulses intended for further peripheral devices,

wherein the electro-magnetic pulses comprise radio frequency waves and the sensor/emitter comprises an antenna lead incorporated into a cable coupling the pointing device circuitry and the electro-magnetic transceiver to the computer.

3. A computer system comprising:

a computer having a keyboard, monitor, memory, processor, a data bus, and a plurality of I/O ports, wherein each of the plurality of I/O ports is coupled to the data bus;

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, wherein the pointing device circuitry is electrically coupled to the data bus of the computer for transmitting the position and command state of the pointing device to the processor and further coupled to a first I/O plug by means of a first plurality of wires, and the first I/O plug is coupled to a first one of the plurality of I/O ports; and

an electro-magnetic transceiver positioned within the housing for converting electro-magnetic pulses to and from electrical signals representative of commands and data, and electrically coupled to the data bus for relaying the electrical signals between the transceiver and the processor, the transceiver being coupled to a second I/O port plug by means of a second plurality of wires, and the second I/O port plug being coupled to a second one of the plurality of I/O ports, the transceiver comprising a sensor/emitter for sending and receiving electro-magnetic pulses between the transceiver and devices having corresponding sensor/emitters,

wherein the first and second plurality of wires are bound together into a cable having the first and second I/O port plugs separated out of the cable at one end, the electro-magnetic pulses comprise radio frequency waves, and the sensor/emitter comprises an antenna lead incorporated into the cable.

4. A computer system comprising:

a computer having a keyboard, monitor, memory, processor, and a data bus;

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, wherein the pointing device circuitry is electrically coupled to the data bus of the computer for transmitting the position and command state of the pointing device to the processor; and

an electro-magnetic transceiver positioned within the housing for converting electro-magnetic pulses to and from electrical signals representative of commands and data, and electrically coupled to the data bus for relaying the electrical signals between the transceiver and the processor, the transceiver comprising a sensor/emitter for sending and receiving electro-magnetic pulses between the transceiver and devices having corresponding sensor/emitters,

wherein the electro-magnetic pulses comprise radio frequency waves and the sensor/emitter comprises an antenna lead incorporated into a cable coupling the pointing device circuitry and the electro-magnetic transceiver to the computer.

5. A peripheral device for a computer comprising:

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, the circuitry coupled to a first I/O port plug by means of a first plurality of wires, and the first I/O port plug further coupled to a first I/O

port on the computer for transmitting the position and command state of the pointing device to a processor in the computer; and

an electro-magnetic transceiver positioned within the housing for converting radio frequency waves to and from electrical signals, the transceiver coupled to a second I/O port plug by means of a second plurality of wires, and the second I/O port plug further coupled to a second I/O port on the computer for relaying such electrical signals between the transceiver and the processor,

wherein the first and second plurality of wires are bound together into a cable having the first and second I/O port plugs separated out of the cable at one end, and an antenna lead is incorporated into the cable and functions as a sensor/emitter for sending and receiving radio frequency waves intended for further peripheral devices.

6. A peripheral device for a computer comprising:

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, the circuitry coupled to a cable which is further coupled to an intelligent serial I/O bus in the computer for transmitting the position and command state of the pointing device to a processor in the computer; and

an electro-magnetic transceiver positioned within the housing for converting radio frequency waves to and from electrical signals, the transceiver coupled to the cable for relaying such electrical signals between the transceiver and the processor,

wherein an antenna lead is incorporated into the cable and functions as a sensor/emitter for sending and receiving radio frequency waves intended for further peripheral devices.

7. A computer system comprising:

a computer having a keyboard, monitor, memory, processor, a data bus, and plurality of I/O ports, wherein the data bus and each of the plurality of I/O ports is coupled to the data bus;

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, the circuitry coupled to a first I/O port plug by means of a first plurality of wires, and the first I/O port plug further coupled to a first one of the plurality of I/O ports for transmitting the position and command state of the pointing device to the processor; and

an electro-magnetic transceiver positioned within the housing for converting radio frequency waves to and from electrical signals, the transceiver coupled to a second I/O port plug by means of a second plurality of wires and the second I/O port plug further coupled to a second one of the plurality of I/O ports for relaying the electrical signals between the transceiver and the processor,

wherein the first and second plurality of wires are bound together into a cable having the first and second I/O port plugs separated out of the cable at one end, and an antenna lead is incorporated into the cable and functions as a sensor/emitter for sending and receiving radio frequency waves between the transceiver and devices having corresponding sensor/emitters.

8. A computer system comprising:

a computer having a keyboard, monitor, memory, processor, a data bus, and an intelligent serial I/O bus,

9

wherein the processor and the serial I/O bus are coupled to the data bus;

a pointing device having a housing and circuitry within the housing to determine the position and command state of the pointing device, the pointing device circuitry coupled to a cable which is further coupled to the serial I/O bus for transmitting the position and command state of the pointing device to the processor; and  
an electro-magnetic transceiver positioned within the housing for converting radio frequency waves to and

5

10

from electrical signals, the transceiver coupled to the cable for relaying the electrical signals between the transceiver and the processor,

wherein an antenna lead is incorporated into the cable and functions as a sensor/emitter for sending and receiving radio frequency waves between the transceiver and devices having corresponding sensor/emitters.

\* \* \* \* \*